Ref	Num	Sch Name	BGA	Type	Description	Pin Mux									
						Mode 0	Mode 1	Mode 2	Mode 3	Mode 4	Mode 5	Mode 6	Mode 7	Mode 8	Mode 9
J10	1	VIN_5V		PWR	5V Power supply										
	2	V3_3D		PWR	3.3V power output form PMU										
	3	GND		GND	Ground										
	4	GND		GND	Ground										
	5	SPI4_CS0	N25	IO	SPI4 Chip Select 0	spi4_cs0						ehrpwm3_tripzone_input	gpio5_7		
	6	UART3_TXD	H24	IO	UART3 Transmit Data	uart3_txd				pr0_pru0_gpo19	pr0_pru0_gpi19	ehrpwm4B	gpio5_3		
	7	SPI4_SCLK	P25	IO	SPI4 Clock	spi4_sclk						ehrpwm0_synci	gpio5_4		
	8	UART3_RXD	H25	IO	UART3 Receive Data	uart3_rxd				pr0_pru0_gpo18	pr0_pru0_gpi18	ehrpwm4A	gpio5_2		
	9	SPI4_D0	R24	IO	SPI4 Data line 0	spi4_d0						ehrpwm3_synci	gpio5_5		
	10	UART3_RTSn	K24	O	UART3 Request to Send	uart3_rtsn	hdq_sio			pr0_pru1_gpo19	pr0_pru1_gpi19	ehrpwm5B	gpio5_1		
	11	SPI4_D1	P24	IO	SPI4 Data line 1	spi4_d1						ehrpwm0_tripzone_input	gpio5_6		
	12	UART3_CTSn	H22	IO	UART3 Clear to Send	uart3_ctsn		spi4_cs1		pr0_pru1_gpo18	pr0_pru1_gpi18	ehrpwm5A	gpio5_0		
	13	GND		GND	Ground										
	14	CAN1_RX	L21	IOD	DCAN1 Receive Data	uart1_txd	mmc2_sdwp	dcan1_rx	I2C1_SCL		pr1_uart0_txd	pr1_pru0_gpi16	gpio0_15		
	15	SPI0_CS0	T20	IO	SPI0 Chip Select0	spi0_cs0	mmc2_sdwp	I2C1_SCL	ehrpwm0_synci	pr1_uart0_txd	pr0_uart0_txd	pr1_edio_data_out1	gpio0_5	ehrpwm1B	
	16	CAN1_TX	K21	IOD	DCAN1 Transmit Data	uart1_rxd	mmc1_sdwp	dcan1_tx	I2C1_SDA		pr1_uart0_rxd	pr1_pru0_gpi16	gpio0_14		
	17	SPI0_SCLK	P23	IO	SPI0 Clock	spi0_sclk	uart2_rxd	I2C2_SDA	ehrpwm0A	pr1_uart0_cts_n	pr0_uart0_cts_n	EMU2	gpio0_2		
	18	CAN0_RX	L22	IOD	DCAN0 Receive Data	uart1_rtsn	timer5	dcan0_rx	I2C2_SCL	spi1_cs1	pr1_uart0_rts_n	pr1_edc_latch1_in	gpio0_13		
	19	SPI0_D0	T22	IO	SPI0 Data line 0	spi0_d0	uart2_txd	I2C2_SCL	ehrpwm0B	pr1_uart0_rts_n	pr0_uart0_rts_n	EMU3	gpio0_3		
	20	CAN0_TX	K22	IOD	DCAN0 Transmit Data	uart1_ctsn	timer6	dcan0_tx	I2C2_SDA	spi1_cs0	pr1_uart0_cts_n	pr1_edc_latch0_in	gpio0_12		
	21	SPI0_D1	T21	IO	SPI0 Data line 1	spi0_d1	mmc1_sdwp	I2C1_SDA	ehrpwm0_tripzone_input	pr1_uart0_rxd	pr0_uart0_rxd	pr1_edio_data_out0	gpio0_4	ehrpwm1A	
	22	GND		GND	Ground										
	23	GND		GND	Ground										
	24	UART0_TXD	J24	O	UART0 Transmit Data	uart0_txd	spi1_cs1	dcan0_rx	I2C2_SCL	eCAP1_in_PWM1_out	pr0_pru1_gpo5	pr0_pru1_gpi5	gpio1_11		
	25	CLKOUT2	C24	O	Clock out 2	xdma_event_intr1	spi0_cs2	tclkin	clkout2	timer7	pr1_pru0_gpi16	EMU3	gpio0_20	pr1_mdio_mdclk	gpio5_29
	26	UART0_RXD	K25	I	UART0 Receive Data	uart0_rxd	spi1_cs0	dcan0_tx	I2C2_SDA	eCAP2_in_PWM2_out	pr0_pru1_gpo4	pr0_pru1_gpi4	gpio1_10		
	27	I2C1_SCL	G20	IOD	I2C1 Clock	I2C1_SCL					pr1_mii0_crs		gpio5_10		
	28	UART0_RTSn	J25	O	UART0 Request to Send	uart0_rtsn	uart4_txd	dcan1_rx	I2C1_SCL	spi1_d1	spi1_cs0	pr1_edc_sync1_out	gpio1_9		
	29	I2C1_SDA	E25	IOD	I2C1 Data	I2C1_SDA					pr1_mii0_rxlink		gpio5_12		
	30	UART0_CTSn	L25	I	UART0 Clear to Send	uart0_ctsn	uart4_rxd	dcan1_tx	I2C1_SDA	spi1_d0	timer7	pr1_edc_sync0_out	gpio1_8		
	31	UART2_TXD	AE23	IO	UART2 Transmit Data	cam1_data5	uart1_dsrn	uart2_txd	mmc2_dat1	pr0_pru1_gpo13	pr0_pru1_gpi13	pr1_edio_latch_in	gpio4_19		
	32	UART5_TXD	A16	O	UART5 Transmit Data	rmii1_refclk	xdma_event_intr2	spi1_cs0	uart5_txd	mcasp1_axr3	mmc0_pow	mcasp1_ahclkx	gpio0_29		
	33	UART2_RXD	AD22	IO	UART2 Receive Data	cam1_data4	uart1_rin	uart2_rxd	mmc2_dat0	pr0_pru1_gpo12	pr0_pru1_gpi12	pr1_edc_latch1_in	gpio4_18	uart0_dcdn	
	34	UART5_RXD	D16	I	UART5 Receive Data	gmii1_col	rmii2_refclk	spi1_sclk	uart5_rxd	mcasp1_axr2	mmc2_dat3	mcasp0_axr2	gpio3_0		gpio0_0
	35	SYS_RESETn			System reset, low active										
	36	UART5_RTSn	B13	O	UART5 Request to Send	gmii1_rxer	rmii1_rxer	spi1_d1	I2C1_SCL	mcasp1_fsx	uart5_rtsn	uart2_txd	gpio3_2		
	37	PMIC_PB			Coin Cell battery input										
	38	UART5_CTSn	B14	I	UART5 Clear to Send	gmii1_crs	rmii1_crs_dv	spi1_d0	I2C1_SDA	mcasp1_aclkx	uart5_ctsn	uart2_rxd	gpio3_1		
	39	GND		GND	Ground										
	40	GND		GND	Ground										
J11	1	VIN_5V		PWR	5V Power supply input										
	2	V3_3D		PWR	3.3V power output form PMU										
	3	GND		GND	Ground										
	4	GND		GND	Ground										
	5	VDDA_ADC1		PWR	Analog power output voltage 1.8V										
	6	VDDA_ADC0		PWR	Analog power output voltage 1.8V										
	7	GND		GND	Ground										
	8	GND		GND	Ground										
	9	ADC1_AIN0	AC16	A	Analog Input/Output 0	ADC1_AIN0									
	10	V1_8D		PWR	1.8V power output form PMU										
	11	ADC1_AIN1	AB16	A	Analog Input/Output 1	ADC1_AIN1									
	12	GPIO5_9	F24	IO	GPIO5_9						pr1_mii1_col		GPIO5_9		
	13	ADC1_AIN2	AA16	A	Analog Input/Output 2	ADC1_AIN2									
	14	GPIO5_8	D25	IO	GPIO5_8						pr1_mii0_col		gpio5_8		
	15	ADC1_AIN3	AB15	A	Analog Input/Output 3	ADC1_AIN3									
	16	I2C0_SCL	Y22	IOD		I2C0_SCL	timer7	uart2_rtsn	eCAP1_in_PWM1_out				gpio3_6		
	17	ADC1_AIN4	AA15	A	Analog Input/Output 4	ADC1_AIN4									
	18	I2C0_SDA	AB24	IOD		I2C0_SDA	timer4	uart2_ctsn	eCAP2_in_PWM2_out				gpio3_5		
	19	ADC1_AIN5	Y15	A	Analog Input/Output 5	ADC1_AIN5									
	20	GND		GND	Ground										
	21	ADC1_AIN6	AE16	A	Analog Input/Output 6	ADC1_AIN6									
	22	CLKOUT1	D24	O	Clock out 1	xdma_event_intr0	ext_hw_trigger	timer4	clkout1	spi1_cs1	pr1_pru0_gpi16	EMU2	gpio0_19	pr1_mdio_data	gpio5_28
	23	ADC1_AIN7	AD16	A	Analog Input/Output 7	ADC1_AIN7									
	24	MCASP0_AHCLKR	M24	IO	McASP0 Receive Master Clock	mcasp0_ahclkr	ehrpwm0_synci	mcasp0_axr2	spi1_cs0	eCAP2_in_PWM2_out	pr0_pru0_gpo3	pr0_pru0_gpi3	gpio3_17		
	25	GND		GND	Ground										
	26	MCASP0_AHCLKX	L24	IO	McASP0 Transmit Master Clock	mcasp0_ahclkx	eQEP0_strobe	mcasp0_axr3	mcasp1_axr1	EMU4	pr0_pru0_gpo7	pr0_pru0_gpi7	gpio3_21		gpio0_3
	27	MMC2_CLK	A12	IO	MMC/SD/SDIO Clock	gpmc_clk		gpmc_wait1	mmc2_clk	pr1_mii1_crs	pr1_mdio_mdclk	mcasp0_fsr	gpio2_1		gpio0_4
	28	MCASP0_ACLKX	N24	IO	McASP0 Transmit Bit Clock	mcasp0_aclkx	ehrpwm0A	spi0_cs3	spi1_sclk	mmc0_sdcd	pr0_pru0_gpo0	pr0_pru0_gpi0	gpio3_14		
	29	MMC2_CMD	B12	IO	MMC/SD/SDIO Command	gpmc_csn3	gpmc_wait0	qspi_clk	mmc2_cmd	pr1_mii0_crs	pr1_mdio_data	EMU4	gpio2_0	gmii2_crs	rmii2_crs_dv
	30	MCASP0_ACLKR	L23	IO	McASP0 Receive Bit Clock	mcasp0_aclkr	eQEP0A_in	mcasp0_axr2	mcasp1_aclkx	mmc0_sdwp	pr0_pru0_gpo4	pr0_pru0_gpi4	gpio3_18		gpio0_18
	31	MMC2_DAT0	C5	IO	MMC/SD/SDIO Data line 0	gpmc_a1	gmii2_rxdv	rgmii2_rctl	mmc2_dat0	gpmc_a17	pr1_mii1_rxdv	ehrpwm0_synco	gpio1_17		
	32	MCASP0_FSX	N22	IO	McASP0 Transmit Frame Sync	mcasp0_fsx	ehrpwm0B	spi1_cs2	spi1_d0	mmc1_sdcd	pr0_pru0_gpo1	pr0_pru0_gpi1	gpio3_15		
	33	MMC2_DAT1	C6	IO	MMC/SD/SDIO Data line 1	gpmc_a2	gmii2_txd3	rgmii2_td3	mmc2_dat1	gpmc_a18	pr1_mii1_txd3	ehrpwm1A	gpio1_18		
	34	MCASP0_FSR	K23	IO	McASP0 Receive Frame Sync	mcasp0_fsr	eQEP0B_in	mcasp0_axr3	mcasp1_fsx	EMU2	pr0_pru0_gpo5	pr0_pru0_gpi5	gpio3_19		gpio0_19
	35	MMC2_DAT2	A4	IO	MMC/SD/SDIO Data line 2	gpmc_a3	gmii2_txd2	rgmii2_td2	mmc2_dat2	gpmc_a19	pr1_mii1_txd2	ehrpwm1B	gpio1_19		
	36	MCASP0_AXR0	H23	IO	McASP0 Serial Data (IN/OUT)	mcasp0_axr0	ehrpwm0_tripzone_input	spi1_cs3	spi1_d1	mmc2_sdcd	pr0_pru0_gpo2	pr0_pru0_gpi2	gpio3_16		
	37	MMC2_DAT3	A3	IO	MMC/SD/SDIO Data line 3	gpmc_be1n	gmii2_col	gpmc_csn6	mmc2_dat3	gpmc_dir	pr1_mii1_col	mcasp0_aclkr	gpio1_28		
	38	MCASP0_AXR1	M25	IO	McASP0 Serial Data (IN/OUT)	mcasp0_axr1	eQEP0_index	mcasp1_axr0	EMU3	pr0_pru0_gpo6	pr0_pru0_gpi6	gpio3_20	gpio0_2		
	39	GND		GND	Ground										
	40	GND		GND	Ground										